

VETENSKAPLIGA PUBLIKATIONER/SCIENTIFIC PUBLICATIONS 2007

Elektroniska komponenter/Electronic Devices

Journals and International Conferences

- B. Mesgarzadeh, M. Hansson, and A. Alvandpour, "Jitter characteristic in charge recovery resonant clock distribution", in IEEE Journal of Solid-State Circuits, pp. 1618-1625, vol. 42, July 2007.
- R. Ramzan, S. Andersson, J. Dabrowski, and C. Svensson, "A 1.4V 25mW inductorless wideband LNA in 0.13 μm CMOS", in proc. IEEE International Solid State Circuits Conference (ISSCC'07), pp. 12-13, San Fransisco, USA, 2007.
- S. Vangal, et al, "An 80-Tile 1.28TFLOPS Network-on-Chip in 65nm CMOS", in proceedings of IEEE International Solid State Circuits Conference (ISSCC'07), pp. 98-99, San Fransisco, USA, 2007.
- S. Vangal, A. Singh, J. Howard, S. Dighe, N. Borkar, and A. Alvandpour, "A 5.1 GHz 0.34 mm² Router for Network-on-Chip Applications", 2007 Symposium on VLSI Circuits, Digest of Technical Papers, pp. 42-43, Kyoto, Japan, 2007.
- J. Dabrowski and R. Ramzan, "Boosting SER test for RF transceivers by simple DSP techniques", in proc. IEEE Design Automation and Test in Europe Conference (DATE'07), pp. 719-724, Nice, France, 2007.
- M. Hansson, and A. Alvandpour, "Comparative analysis of process variation impact on flip-flop power performance", in proc. IEEE International Symposium on Circuits and Systems, pp. 3744-3747, New Orleans, USA, 2007.
- B. Mesgarzadeh, M. Hansson and A. Alvandpour, "Low-power bufferless resonant clock distribution networks", in proceedings of IEEE Meadwest Symposium on Circuits and Systems (MWSCAS'07), pp. 960-963, Montreal, Canada, 2007.
- H. Fredriksson, C. Svensson, and A. Alvandpour, "A 3.4 GB/S low latency 1 bit input digital FIR-filter in 0.13 μm CMOS", in proc. Mixed design of integrated circuits and systems (MIXDES'07), pp. 181-184, Ciechocinek, Poland, 2007.
- T. Sundström, and A. Alvandpour: "A kick-back reduced comparator for a 4-6-bit 3-GS/S flash and in a 90nm CMOS process", in proc. Mixed design of integrated circuits and systems (MIXDES'07), pp. 195-198, Ciechocinek, Poland, 2007.
- H. Fredriksson and C. Svensson, "3-Gb/s, Single-ended Adaptive Equalization of Bidirectional Data over a Multi-drop Bus", in proc. International Symposium on System-on-Chip, pp. 125-128, Tampere, Finland, 2007.
- N. Ahsan, A. Ouacha, C. Samuelsson, and T. Boman, "Applications of programmable microwave function array (PROMFA)", in proc. European Conference on Circuit Theory and Design (ECCTD'07), pp. 164-167, Sevilla, Spain, 2007.

S. Andersson, I. Carlsson, S. Natarajan, and A. Alvandpour, "A 128Kb 5T SRAM in 0.18mm CMOS", International Conference on Memory Technology and Design (ICMTD'07), pp. 185-188, Giens, France, 2007.

S. Andersson, R. Ramzan, J. Dabrowski, and C. Svensson, "Multiband direct RF-sampling receiver front-end for WLAN in 0.13 μm CMOS", in proc. European Conference on Circuit Theory and Design (ECCTD'07), pp. 168-171, Sevilla, Spain, 2007.

A. Kashif, T. Johansson, C. Svensson, and Q. Wahab: "A new large signal simulation technique to study non-linear effects of microwave power transistor", in proc. International Semiconductor Device Research Symposium (ISDRS'07), Maryland, USA, 2007.

N. Ahsan, A. Ouacha, J. Dabrowski, and C. Samuelsson, "Dual band tunable LNA for flexible RF front-end", in proceedings of International Bhurban Conference on Applied Sciences & Technology (IBCAST'07), 2007, Islamabad, Pakistan, 2007.

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N. Ahsan, A. Ouacha, C. Samuelsson and T. Boman, "A widely tunable filter using generic PROMFA cells", in proc. Swedish System-on-Chip Conference (SSoCC'07), Fiskebäckskil, 2007.

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M. Hansson and A. Alvandpour: "Impact of process variation on flip-flop power-performance in 90nm CMOS", in proc. Swedish System-on-Chip Conference (SSoCC'07), Fiskebäckskil, 2007.

A. Kashif, T. Johansson, C. Svensson, T. Arnborg and Q. Wahab: "Enhancement in RF performance of LDMOS transistor utilizing large signal TCAD physical simulation", in proc. Conference on RF measurement technology (RFMTC'07), Gävle, 2007.

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Dissertations – Compilations

S. Vangal, "Performance and Energy Efficient Network-on-Chip Architectures. Linköping Studies in Science and Technology, Dissertation No. 1130, ISBN: 978-91-85895-91-5, Linköping, October 2007.

Licentiate Thesis

H. Fredriksson, "Equalization Techniques for Multi-Gb/s Multi-drop Buses", Thesis No. 1298, LiU-TEK-LIC-2007:7, Linköping University, Linköping 2007, ISBN: 978-91-85715-80-0.

Teaching materials – Other

J. Dabrowski, "D/T and BiST Techniques for RF Integrated Circuits. Tutorial presented at ENSICAEN/NXP, Caen, France, 2007.

C. Svensson, "Multi-standard challenges and solutions", presented at ESSDERC,ESSCIRC 2007 tutorial SDR-concepts for multi-standard mobile phones at Munich, 2007.