

PUBLICATIONS

Electronic Devices 2002

1. S. Andersson, P. Caputa, C. Svensson: "A tuned, inductorless, recursive filter LNA in CMOS". Proc. of the 28th European solid-state circuit conference, Florence, Italy, 2002.
2. H. Bengtsson, C. Svensson: "Speed study of a 2,5 Gb/s equalizer for optical communication in a 3 V 0.35 um cmos process". Proc. of NORCHIP, Copenhagen, Denmark, 2002.
3. P. Caputa och C. Svensson: "Low-power, low-latency global interconnect". Proc. of IEEE ASIC/SOC 2002 conference, pp. 384-389, Rochester, September 2002.
4. J.J. Dabrowski: "Efficient post-layout timing verification via RLC trees and explicit PWL timing integration". Proc. of ICECS 2002 conference, Dubrovnik, Croatia, September 2002.
5. J. Elbornsson, K. Folkesson, J-E Eklund: "Measurement verification of estimation method for time errors in a time-interleaved A/D converter system". Proc. of IEEE Int Symp. Circuits Syst., pp. 129-132, Scottsdale, USA, May 26-29, 2002.
6. J. Elbornsson, F. Gustafsson, J-E Eklund: "Amplitude and gain error influence on time error estimation algorithm for time interleaved A/D converter system. Proc. of IEEE Conf on Acoust, Speech, and Sign Proc, pp 1281-1284, Orlando, Florida, 2002.
7. D. Jakonis, C. Svensson: "A 1 GHz linearized CMOS track-and-hold circuit". Proc. of the IEEE ISCAS, vol 5, pp. 577-580, Scottsdale, Arizona, 2002.
8. D. Jakonis, K. Folkesson, C. Svensson: "An RF sampling front-end for a digital receiver". Proc. NORCHIP, Copenhagen, Denmark, 2002.
9. K. Folkesson, J-E Eklund, C. Svensson: "Relevance of using single-tone tests to characterize ADCs for ADSL modems". Proc. of NORCHIP, Copenhagen, Denmark, 2002.
10. Rantzer, C. Svensson: „Bulk wafer defects observable in vision chips“. Proc. of the 32nd European solid-state device research conference, pp. 659-662, Florence, Italy, 2002.
11. Söderquist: "Globally updated mesochronous design style (GUM-design-style)". Proc. of the 28th European solid-state circuit conference, pp. 603-606, Florence, Italy, 2002.
12. Söderquist: "Expandable high throughput vector based access memory architecture". Proc. of the 28th European solid-state circuit conference, pp. 599-602, Florence, Italy, 2002.
13. R. Jonsson, J. Eriksson, Q. Wahab, S. Rudner, N. Rorsman, H. Zirath, C. Svensson: "Evaluation of SiC MESFET structures using large-signal time-domain simulations", International Conference on Silicon Carbide and Related Materials 2001, Materials Science Forum, vol. 389-393, p. 1395, 2002.
14. S. Rusu, M. Sachdev, C. Svensson, B. Nauta: "Trends and challenges in VLSI technology scaling towards 100 nm", Proceedings of 7th Asia and South Pacific Design Automation Conference and 15h International Conference on VLSI Design, p. 16, 2002.
15. R. Aljasmí, J. Kjellgren: "A study of FMCW-radar for high accuracy measurement". Proc. of Radio Vetenskap och Kommunikation 02, Stockholm, June 2002.
16. S. Andersson, C. Svensson: "10 GHz wideband low-noise amplifier using a 0.35um SiGe BiCMOS technology. Proc. System-on-Chip conference, Falkenberg, March, 2002.
17. H. Bengtsson, C. Svensson: "2.5 Gb/s equalizer for optical communication . Proc. Swedish System-on-Chip conference, Falkenberg, March, 2002.
18. P. Caputa, C. Svensson: "Low power, low latency global interconnect". Proc. of Swedish System-on-Chip conference, Falkenberg, March 2002.

19. H. Eriksson, T. Henriksson, P. Larsson-E: "Full-custom vs standard-cell based design – An adder comparison". Proc. of the 2002 Swedish System-on-Chip conference, Falkenberg, Sweden, March, 2002.
20. D. Jakonis, K. Folkesson, C. Svensson: "RF sampling receiver". Proc. of the SSoCC'02 Falkenberg, Sweden, March 2002.
21. C. Svensson, P. Caputa, S. Andersson: "A tuned, inductorless, recursive filter LNA in CMOS". Proc. of Radio Vetenskap och Kommunikation 02, Stockholm, June 2002.
22. D. Jakonis: "Signal readout and sampling in CMOS". Linköping University, Thesis No. 967, LIU-TEK-LIC-2002:40, 2002.
23. Söderquist: "CMOS circuits for digital RF systems" Linköping University, Dissertation No. 775, ISBN: 91-7373-429-2, 2002.
24. M. Duppils: "Digitally controlled analog multiply-accumulate units". Linköping University, Dissertation No. 792. ISBN: 91-7373-478-0, 2002.