

VETENSKAPLIGA PUBLIKATIONER/SCIENTIFIC PUBLICATIONS 2013

Elektroniska komponenter/Electronic Devices

Journals and International Conferences

A. Bhide, O. E. Najari, B. Mesgarzadeh, and A. Alvandpour, "An 8-GS/s 200-MHz Bandwidth 68-mW $\Delta\Sigma$ DAC in 65-nm CMOS", in IEEE Transactions on Circuits and Systems II: Express Briefs, vol. 60, pp. 387-391, July 2013.

A. Fazli Yeknami, F. Qazi and A. Alvandpour, "Low-Power DT $\Delta\Sigma$ Modulators Using SC Passive Filters in 65nm CMOS" in IEEE Transactions on Circuits and Systems I: Regular papers, pp. 1-13, 2013.

Y. Jung, J. Fritzin, M. Enqvist, and A. Alvandpour, "Least-Squares Phase Predistortion of a +30 dBm Class-D Outphasing RF PA in 65 nm CMOS " in IEEE Transactions on Circuits and Systems I: Regular papers, pp. 1915-1928, vol. 60, nr. 7, July 2013.

E. Nilsson and C. Svensson, "Ultra Low Power Wake-Up Radio using Envelope Detector and Transmission Line Voltage Transformer", in IEEE Journal on Emerging and Selected Topics in Circuits and Systems Volume 3, Issue 1, pp. 5-12 , March 2013.

C. Xu, M. Persson, H. Chen, S. Karlsson, M. Danielsson, C. Svensson, and H. Bornefalk. "Evaluation of a Second-Generation Ultra-Fast Energy-Resolved ASIC for Photon-Counting Spectral CT", in IEEE Transactions on Nuclear Science, Volume 60, Issue 1, pp. 437-445, February 2013.

C. Xu, H. Chen, M. Persson, , S. Karlsson, M. Danielsson, C. Svensson, and H. Bornefalk. "Energy resolution of a segmented silicon strip detector for photon-counting spectral CT " in IEEE Transactions on Nuclear Science, Volume 60, Issue 1, vol. 715, pp. 11-17, 2013.

H. Bornefalk, M. Persson, C. Xu, S. Karlsson, C. Svensson, and M. Danielsson, "Effect of Temperature Variation on the Energy Response of a Photon Counting Silicon CT Detector", in IEEE Transactions on Nuclear Science, Volume 60, , Issue 2, pp. 1442-1449, April 2013.

A. Fazli Yeknami and A. Alvandpour, "A 2.1 uW 80 dB SNR DT Delta-Sigma Modulator for Medical Implant Devices", in Journal of Analog Integrated Circuits and Signal Processing, vol. 77, pp. 69-78, October 2013.

D. Svärd, C. Jansson, and A. Alvandpour, "A readout IC for an uncooled microbolometer infrared FPA with on-chip self-heating compensation in 0.35 μm CMOS", in Analog Integrated Circuits and Signal Processing, vol. 77, pp. 29 - 44, October 2013.

H. Reza Khan, J. Fritzin, M. Enqvist, A. Alvandpour, and Q. Ul Wahab, "A Parallel Circuit Differential Class-E Power Amplifier Using Series Capacitance", in Analog Integrated Circuits and Signal Processing, vol. 75, pp. 31-40, 2013.

A. Ojani, B. Mesgarzadeh, and A. Alvandpour, "A Quadrature UWB Frequency Synthesizer with Dynamic Settling-Time Calibration", IEEE Intl. Symp. Circuits and Systems (ISCAS), pp. 2480-2483, May 2013.

A. Fazli Yeknami, and A. Alvandpour, "A 0.5-V 250-nW 65-dB SNDR Passive $\Delta\Sigma$ Modulator for Medical Implant Devices", IEEE Intl. Symp. Circuits and Systems (ISCAS), pp. 2010-2013, May 2013.

A. Fazli Yeknami, and A. Alvandpour, "A Variable Bandwidth Amplifier for a Dual-mode Low-Power $\Delta\Sigma$ Modulator in Cardiac Pacemaker System", IEEE Intl. Symp. Circuits and Systems (ISCAS), pp. 1918-1921, May 2013.

A. Fazli Yeknami and A. Alvandpour, "A 0.7-V 400-nW Fourth-Order Active-Passive Delta-Sigma Modulator with One Active Stage" in 21st IEEE Int. Conf. Very Large Scale Integration (VLSI-SoC), October 2013.

Q-T. Duong and J. Dabrowski, "Focused Calibration for Advanced RF Test with Embedded RF Detectors", in European Conference on Circuit Theory and Design (ECCTD), September 2013.

F. Qazi, Q-T. Duong, and J. Dabrowski, "Blocker and Image Reject Low-IF Frontend", in European Conference on Circuit Theory and Design (ECCTD), September 2013.

R. Ragavan, A. Narayanan, M. Bengtsson, and Quoc-Tai Duong, "A 0.35 μ m CMOS 6-bit Current Steering DAC", in European Conference on Circuit Theory and Design (ECCTD), September 2013.

T. Johansson, O. Bengtsson, S. Lotfi, L. Vestling, H. Norström, J. Olsson, C. Nyström, "A +32.8 dBm LDMOS power amplifier for WLAN in 65 nm CMOS technology", to be presented at EuMIC 2013, October 2013.

C. Svensson, Z. He, H. Zirath, L. Bao, and J. Chen, "Resource efficient implementation of a 10Gb/s radio receiver baseband in FPGA", Proceedings of the 10th FPGA world Conference, Article no 10, ACM, doi10.1145/2513683.2513693, 2013.

M. F. U. Haque, T. Johansson, and D. Liu, "Combined RF and Multilevel PWM Switch Mode Power Amplifier", Norchip, 2013.

Q-T. Duong, J. Dabrowski, and A. Alvandpour, "Highly linear open-loop output driver design for high speed capacitive DACs", in Norchip, 2013.

T. Johansson, "Att mäta kvalitet i utbildning", Elektronik i Norden, No. 11, p. 8, 2013.

National Conferences

D. Zhang and A. Alvandpour, "A 3-nW 9.1-ENOB SAR ADC at 0.7 V and 1 kS/s", in Swedish System-on-Chip Conference (SSoCC), May 2013. (Best Student Paper Award)

T. Johansson, O. Bengtsson, S. Lotfi, L. Vestling, H. Norström, J. Olsson, and C. Nyström, "A linear 32.8 dBm 2.4 GHz LDMOS power amplifier in 65 nm CMOS", in Swedish System-on-Chip Conference (SSoCC), May 2013.

Quoc-Tai Duong and Jerzy J. Dabrowski, "On-chip IP3 IP2 RF Advanced Test and Calibration Technique with Embedded RF Detectors", in Swedish System-on-Chip Conference (SSoCC), May 2013.