

VETENSKAPLIGA PUBLIKATIONER/SCIENTIFIC PUBLICATIONS 2009

Elektroniska komponenter/Electronic Devices

Journals and International Conferences

- B. Mesgarzadeh and A. Alvandpour, "A Low-Power Digital DLL-Based Clock Generator in Open-Loop Mode", in IEEE Journal of Solid-State Circuits, vol 44, No. 7, pp. 1907-1913, 2009.
- T. Sundström, B. Murmann, and C. Svensson, "Power Dissipation Bounds for High-Speed Nyquist Analog-to-Digital Converters", in Trans. Circuits and Systems I: Regular Papers, Volume 56, Issue 3, pp. 509 - 518, March 2009.
- T. Sundström and A. Alvandpour, "Utilizing Process Variations for Reference Generation in a Flash ADC", in IEEE Trans. Circuits and Systems II: Express Briefs, Volume 56, Issue 5, pp. 364 - 368, May 2009.
- J. Dabrowski and R. Ramzan, "Built-in Loopback Test for IC RF Transceivers", in IEEE Trans. on VLSI Systems, pp. 1-14, December 2009.
- H. Fredriksson and C. Svensson, "Improvement Potential and Equalization Example for Multidrop DRAM Memory Buses", in IEEE Trans. Advanced Packaging, Vol.32, Issue 3, pp: 675-682, August 2009.
- R. Ramzan, S. Andersson, J. Dabrowski, and C. Svensson, "Multiband RF-Sampling Receiver Front-End with On-Chip Testability in 0.13 μ m CMOS", in J. Analog Integrated Circuits and Signal Processing, pp. 115-127, Nov. 2009.
- C. Svensson, "Requirements and challenges for SDR implementation", invited paper in SDR'09 Technical Conference and Product Exhibition, Washington DC, 2009.
- S. Ahmad and J. Dabrowski, "On-Chip Stimuli Generation for ADC Dynamic Test by Sigma-Delta Technique", in Proc. European Conference on Circuit Theory & Design (ECCTD '09), pp. 105-108, Antalya, Turkey, 2009.
- J. Dabrowski, "Fast BER Test for Digital RF Transceivers", in Proc. European Test Symposium, Seville, Spain, 2009.
- B. Mesgarzadeh, M. R. Sadeghifar, P. Fredriksson, C. Jansson, F. Niklaus, A. Alvandpour, "A low-noise circuit in 0.35- μ m CMOS for low-cost uncooled FPA infrared network camera", in proc. SPIE, vol 7298, pp. 72982F-72982F-8, Orlando, USA, 2009.
- J. Fritzin and A. Alvandpour, "Low Voltage Class-E Power Amplifiers for DECT and Bluetooth in 130nm CMOS", in proc. IEEE Topical Meeting on Silicon Monolithic Integrated Circuits in RF Systems (SiRF), pp. 57-60, San Diego, USA, 2009.

National Conferences

- J. Fritzin and A. Alvandpour, "Low-Voltage High-Efficiency Class-E Power Amplifiers in 130nm CMOS for Short-Range Wireless Communications", in proc. Swedish System on Chip Conference (SSoCC'09), Arild, 2009.
- T. Sundström and A. Alvandpour, "A 4-bit 2.5-GS/s 30-mW Flash ADC in 90nm CMOS", in Swedish System on Chip Conference (SSoCC'09), Arild, Sweden, 2009.

R. Ramzan et.al., "0.5-6GHz RF Front-End for Multistandard Receivers in 90nm CMOS with on-Chip Stimulus Generator", in Swedish System on Chip Conference (SSoCC'09), Arild, Sweden, 2009.

Dissertations – Compilations

N. Ahsan, "Reconfigurable and Broadband Circuits for Flexible RF Front Ends" Linköping Studies in Science and Technology. Dissertations, ISBN: 978-91-7393-605-7, Linköping, August 2009.

R. Ramzan, "Flexible Wireless Receivers: On-Chip Testing Techniques and Design for Testability" Linköping Studies in Science and Technology, Dissertations, ISBN: 978-91-7393-601-9, Linköping, June 2009.

Licentiate Thesis

J. Fritzin, "Power Amplifier Circuits in CMOS Technologies", Linköping Studies in Science and Technology, ISBN 978-91-7393-530-2. Linköping, 2009.

T. Sundström, "Design of High-Speed, Low-Power, Nyquist Analog-to-Digital Converters", Linköping Studies in Science and Technology, ISBN 978-91-7393-486-2. Linköping, 2009.